ECAN/LIN PICtail™ Plus Daughter Board User's Guide

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ECAN/LIN PICtail™ PLUS DAUGHTER BOARD USER'S GUIDE

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Preface

NOTICE TO CUSTOMERS

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Documents are identified with a "DS" number. This number is located on the bottom of each page, in front of the page number. The numbering convention for the DS number is "DSXXXXXA", where "XXXXX" is the document number and "A" is the revision level of the document.

For the most up-to-date information on development tools, see the MPLAB® IDE on-line help. Select the Help menu, and then Topics to open a list of available on-line help files.

INTRODUCTION

This chapter contains general information that will be useful to know before using the ECAN/LIN PICtail™ Plus Daughter Board. Items discussed in this chapter include:

- Document Layout
- Conventions Used in this Guide
- Warranty Registration
- Recommended Reading
- The Microchip Web Site
- Development Systems Customer Change Notification Service
- Customer Support
- Document Revision History

DOCUMENT LAYOUT

This document describes how to use the ECAN/LIN PICtail Plus Daughter Board as a development tool to emulate and debug firmware on a target board. The manual layout is as follows:

- **Chapter 1. Introduction** – This chapter introduces the ECAN/LIN PICtail Plus Daughter Board and provides an overview of its features.
- **Chapter 2. Hardware** – This chapter provides a functional overview of the ECAN/LIN PICtail Plus Daughter Board and identifies the major hardware components.
- **Appendix A. Drawings and Schematics** – This appendix provides detailed technical drawings and schematic diagrams of the ECAN/LIN PICtail Plus Daughter Board.

CONVENTIONS USED IN THIS GUIDE

This manual uses the following documentation conventions:

DOCUMENTATION CONVENTIONS

Description	Represents	Examples
Arial font:		
Italic characters	Referenced books	*MPLAB® IDE User's Guide*
	Emphasized text	...is the *only* compiler...
Initial caps	A window	the Output window
	A dialog	the Settings dialog
	A menu selection	select Enable Programmer
Quotes	A field name in a window or dialog	"Save project before build"
Underlined, italic text with right angle bracket	A menu path	*File>Save*
Bold characters	A dialog button	Click **OK**
	A tab	Click the **Power** tab
N'Rnnnn	A number in verilog format, where N is the total number of digits, R is the radix and n is a digit.	4'b0010, 2'hF1
Text in angle brackets < >	A key on the keyboard	Press <Enter>, <F1>
Courier New font:		
Plain Courier New	Sample source code	`#define START`
	Filenames	`autoexec.bat`
	File paths	`c:\mcc18\h`
	Keywords	`_asm, _endasm, static`
	Command-line options	`-Opa+, -Opa-`
	Bit values	`0, 1`
	Constants	`0xFF, 'A'`
Italic Courier New	A variable argument	*`file`*`.o`, where *`file`* can be any valid filename
Square brackets [ ]	Optional arguments	`mcc18 [options]` *`file`* `[options]`
Curly brackets and pipe character: { | }	Choice of mutually exclusive arguments; an OR selection	`errorlevel {0|1}`
Ellipses...	Replaces repeated text	`var_name [, var_name...]`
	Represents code supplied by user	`void main (void) { ... }`

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RECOMMENDED READING

This user's guide describes how to use ECAN/LIN PICtail Plus Daughter Board. Other useful documents are listed below. Microchip documents are available and recommended as supplemental reference resources.

Device Data Sheets

Refer to the data sheet for the specific dsPIC33F or PIC24 device you are using. Specifically, refer to the sections in these data sheets that provide detailed information on the device UART and ECAN modules.

Readme Files

For the latest information on using other tools, read the tool-specific Readme files in the Readmes subdirectory of the MPLAB® IDE installation directory. The Readme files contain update information and known issues that may not be included in this user's guide.

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- **Emulators** – The latest information on Microchip in-circuit emulators.This includes the MPLAB ICE 2000 and MPLAB ICE 4000.
- **In-Circuit Debuggers** – The latest information on the Microchip in-circuit debugger, MPLAB ICD 2.
- **MPLAB® IDE** – The latest information on Microchip MPLAB IDE, the Windows® Integrated Development Environment for development systems tools. This list is focused on the MPLAB IDE, MPLAB SIM simulator, MPLAB IDE Project Manager and general editing and debugging features.
- **Programmers** – The latest information on Microchip programmers. These include the MPLAB PM3 and PRO MATE® II device programmers and the PICSTART® Plus and PICkit™ 1 development programmers.

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- Local Sales Office
- Field Application Engineer (FAE)
- Technical Support

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Technical support is available through the web site at: http://support.microchip.com

DOCUMENT REVISION HISTORY

Revision A (January 2008)

- Initial Release of this Document.

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Chapter 1. Introduction

Thank you for purchasing Microchip Technology's ECAN/LIN PICtail Plus Daughter Board. This board is used with the Explorer 16 Development Board to facilitate rapid implementation and evaluation of applications that use Controller Area Network (CAN) and Local Interconnect Network (LIN) interfaces and are implemented on dsPIC33F Digital Signal Controllers and PIC24 16-bit microcontrollers.

This chapter introduces the ECAN/LIN PICtail Plus Daughter Board and provides an overview of its features. Topics covered include:

- Overview
- Functional Overview

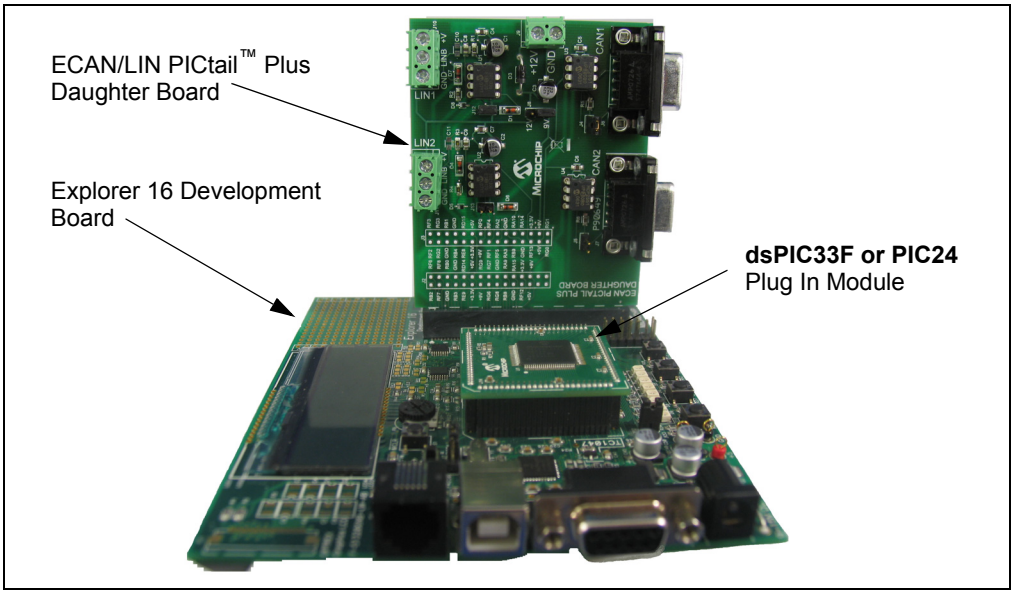
1.1 OVERVIEW

The ECAN/LIN PICtail Plus Daughter Board augments development of dsPIC33F and PIC24 based applications on the Explorer 16 Development Board. It offers two interfaces: one for a CAN Bus and one for a LIN Bus. Although CAN and LIN protocols are used most extensively in automotive applications, the ECAN/LIN PICtail Plus Daughter Board can be used in any application that requires interfacing to a CAN and LIN bus.

Both interfaces can be used simultaneously. Appropriate bus transceivers on the ECAN/LIN PICtail Plus Daughter Board complete the physical layer requirements of the CAN and LIN bus protocols. In both cases, communication is driven by the dsPIC33F or PIC24 device on the Explorer 16 board.

Figure 1-1 shows the ECAN/LIN PICtail Plus Daughter Board plugged into the PICtail Plus slot on the Explorer 16 Development Board. The ECAN/LIN PICtail Plus Daughter Board draws 9V, 5V and 3.3V DC power from the Explorer 16 Development Board. The external power supply and MPLAB ICD 2 are plugged into the Explorer 16 board.

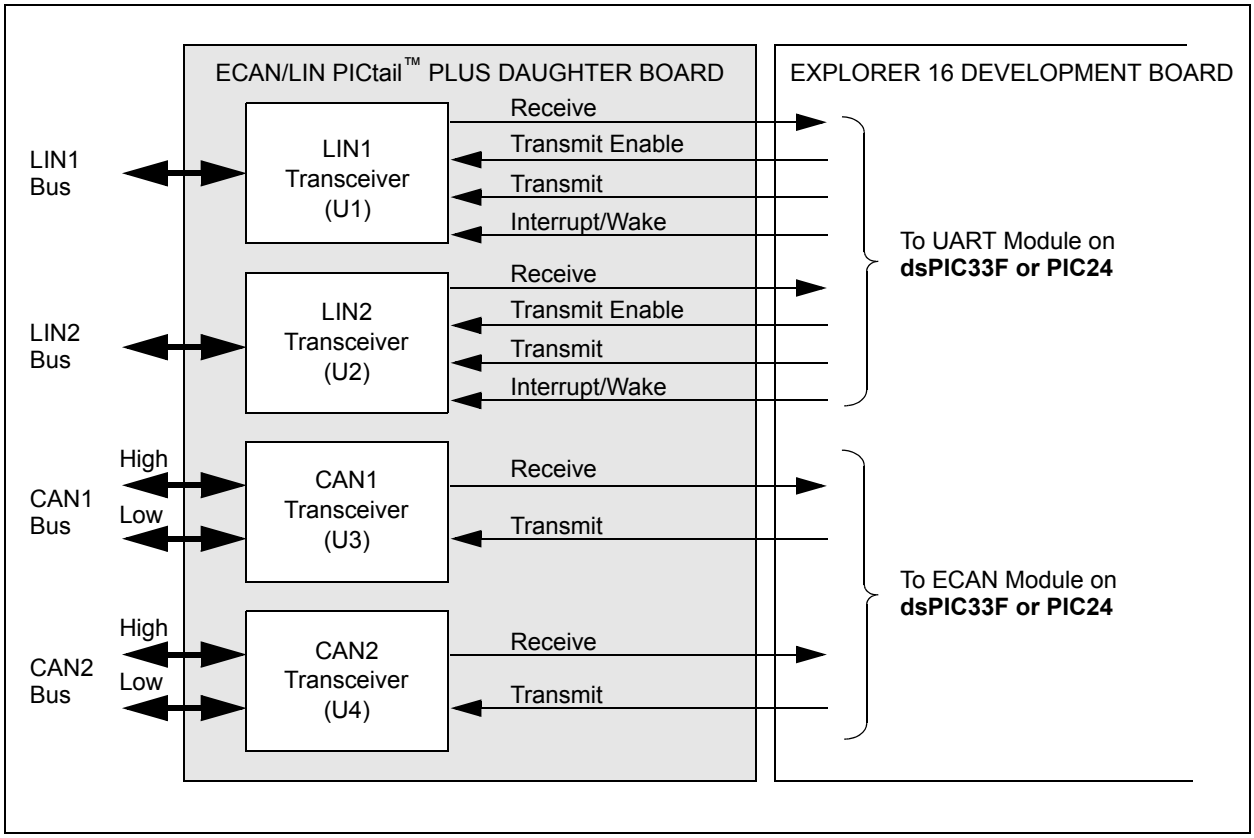
FIGURE 1-1: ECAN/LIN PICtail™ PLUS DAUGHTER BOARD



1.2 FUNCTIONAL OVERVIEW

The block diagram shown in Figure 1-2 illustrates the mainstream operation of the ECAN/LIN PICtail Plus Daughter Board. The board contains two LIN signal conditioning circuits and two CAN signal conditioning circuits. The board also enables power to be provided by the Explorer 16 Development Board or by an external 12V DC source.

FIGURE 1-2: ECAN/LIN PICtail™ PLUS DAUGHTER BOARD BLOCK DIAGRAM



1.2.1 LIN Operation

The ECAN/LIN PICtail Plus Daughter Board connects two LIN transceivers with integrated voltage regulators to UART modules on a dsPIC33F or PIC24 control device on the Explorer 16 board. The LIN transceiver monitors the LIN bus, conditions the incoming signal and passes it to the UART module on the control device. The LIN transceiver responds to a "Transmit Enable" from the control device by conditioning an output signal and placing it on the LIN bus.

A power-down mode turns the transmitter and voltage regulator off, leaving only the receiver and wake-up circuits in operation. Each LIN circuit includes a Master/Slave jumper to accommodate a Master node on the LIN bus.

For detailed information on the MCP2021-330 LIN Transceiver, refer to Microchip Data Sheet MCP202X *"LIN Transceiver with Voltage Regulator"* (DS22018).

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1.2.2 CAN Operation

The ECAN/LIN PICtail Plus Daughter Board connects two high-speed CAN transceivers to ECAN modules on the control device on the Explorer 16 board. The CAN transceivers convert the differential signal on the CAN bus to a digital signal for the ECAN module. It also converts the ECAN output digital signal to a differential signal for the CAN bus.

In Sleep mode, the CAN transmitter is turned off, and the receiver operates at a lower current level. The control device monitors CAN activity and switches the transceiver back to normal operation when needed.

For detailed information on the MCP2551 High-Speed CAN Transceiver, refer to Microchip Data Sheet MCP2551 *"High-Speed CAN Transceiver"* (DS21667).

NOTES:

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Chapter 2. Hardware

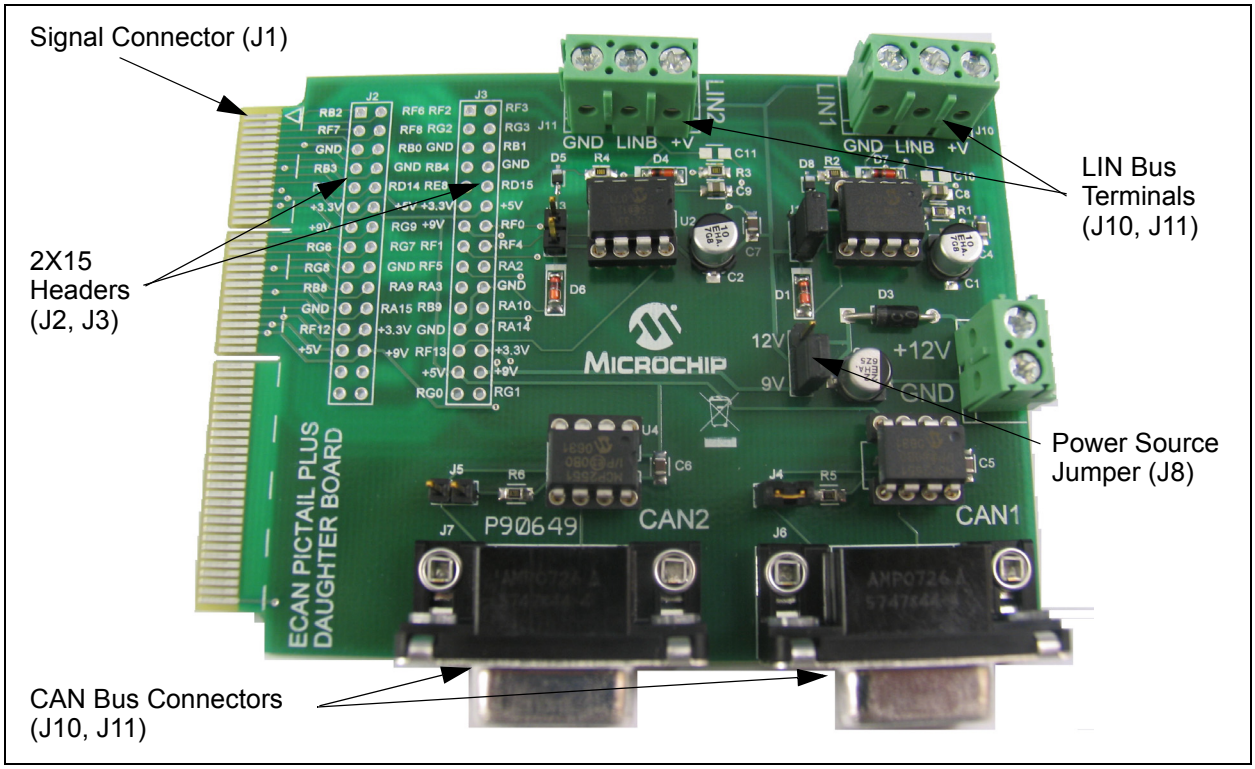
This chapter provides a functional overview of the ECAN/LIN PICtail Plus Daughter Board and identifies the major hardware components. Topics covered include:

- Board Setup
- Hardware Components

2.1 BOARD SETUP

Figure 2-1 is a photograph of ECAN/LIN PICtail Plus Daughter Board. Callouts indicate the CAN bus, LIN bus and signal connections on the daughter board. Also shown are two 2x15 headers that provide signals from the Explorer 16 board. These headers can be probed for development, testing or monitoring of the application.

FIGURE 2-1: ECAN/LIN PICtail™ PLUS DAUGHTER BOARD

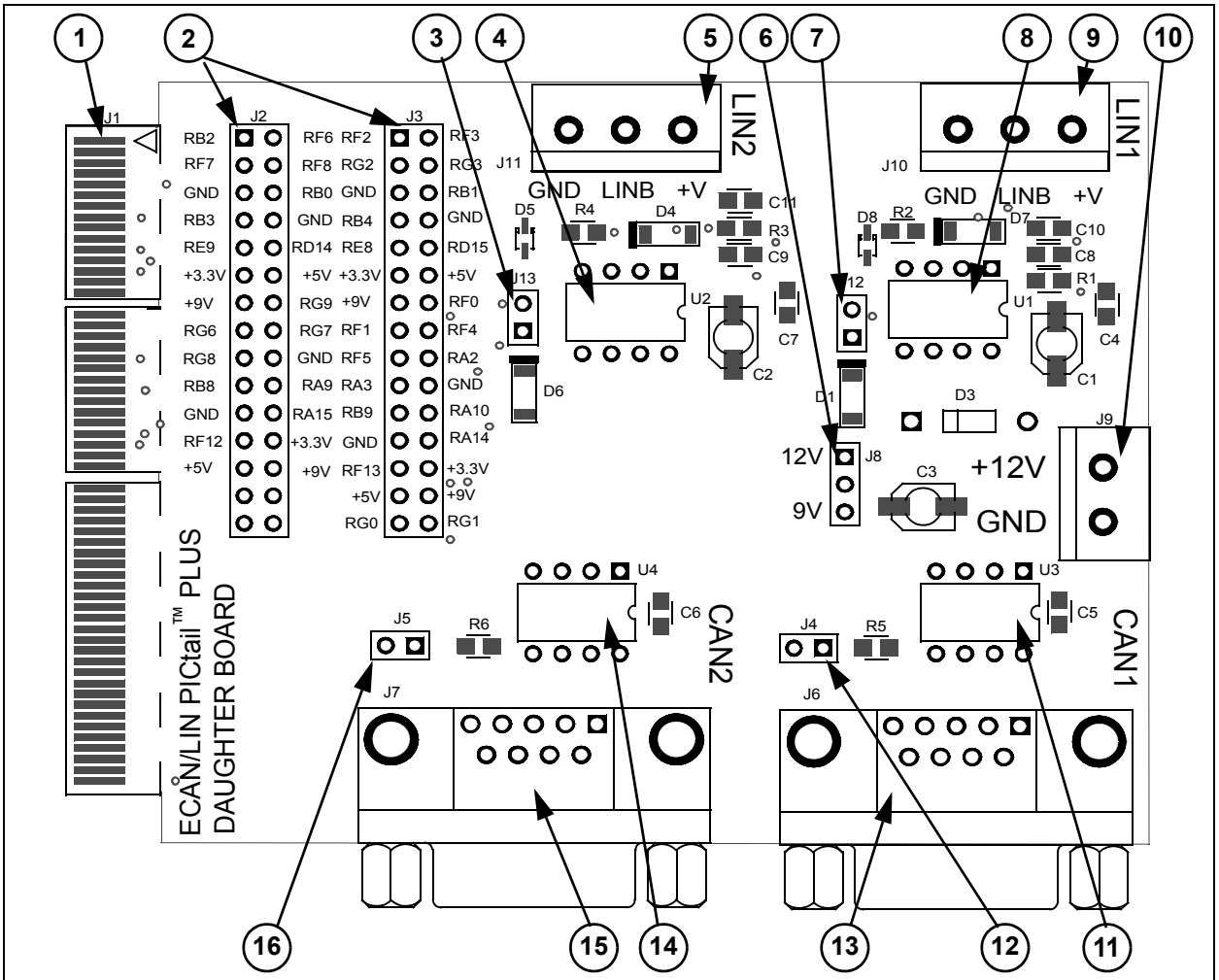


The daughter board obtains 9V DC input power from the Explorer 16 Development Board through the 120-pin signal connector (J1). The power supply to the LIN Bus Transceivers is switchable between the 9V available from Explorer 16 Development Board or an external +12V supply. This external power supply must be connected to J9. Jumper J8 must be set to use the required power supply.

2.2 HARDWARE COMPONENTS

Figure 2-2 identifies the key hardware components on the ECAN/LIN PICtail Plus Daughter Board.

FIGURE 2-2: ECAN/LIN PICtail™ PLUS DAUGHTER BOARD



No.	Description	No.	Description
1	Explorer 16 Board PICtail™ Connector (J1)	9	LIN1 Bus Connector (J10)
2	Auxilliary Headers (J2, J3)	10	LIN External +12V DC Terminal Block (J9)
3	LIN2 Master/Slave Jumper (J13)	11	CAN1 Transceiver (U3)
4	LIN2 Transceiver (U2)	12	CAN1 Bus Termination Jumper (J4)
5	LIN2 Bus Connector (J11)	13	CAN1 Bus Connector (J6)
6	LIN Power Select Jumper (J8)	14	CAN2 Transceiver (U4)
7	LIN1 Master/Slave Jumper (J12)	15	CAN2 Bus Connector (J7)
8	LIN1 Transceiver (U1)	16	CAN2 Bus Termination Jumper (J5)

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2.2.1 Explorer 16 Board PICtail™ Connector (J1)

Explorer 16 Board PICtail connector J1 (see Reference 1) is a 120-pin signal connector that carries signals from the I/O pins of the dsPIC33F or PIC24 device on the Explorer 16 board to the ECAN/LIN PICtail Plus Daughter Board. This connector handles the following signals:

- 9V, 5V and 3.3V DC input to the ECAN/LIN PICtail Plus Daughter Board
- Signals from the ECAN modules on the dsPIC33F or PIC24 device to the CAN transceivers on the ECAN/LIN PICtail Plus Daughter Board
- Signals from the UART modules on the dsPIC33F or PIC24 device to the LIN transceivers on the ECAN/LIN PICtail Plus Daughter Board
- LIN Bus Fault Communication

2.2.2 Auxilliary Headers (J2, J3)

Auxilliary Headers J2 and J3 (see Reference 2) make available a various set of signals from the Explorer 16 Development Board. These headers can be used for monitoring, testing and development purposes. Refer to the Board schematic for more details of these headers.

2.2.3 LIN Bus Master/Slave Select Jumpers (J12, J13)

The LIN1 and LIN2 Bus Master/Slave Select Jumpers (see Reference 3 or Reference 7) are used to indicate the presence of a master device on the LIN Bus. Placing jumper J13 or J12 pulls the LIN Bus high via a 1K pull-up resistor. This can be detected by nodes on the LIN bus as an indication that a Master node is present. There can be only one Master node on a LIN bus.

2.2.4 LIN Transceivers (U1, U2)

A set of MCP2021 LIN Transceivers (see Reference 8 and Reference 4) provide the interface between the UART modules on the dsPIC33F or PIC24 device and the LIN bus. The transceiver converts the signals from the UART modules on the control device to a 5V regulated signal for the LIN Bus. The power supply to the LIN Transceivers is selectable between +12V or +9V with jumper J8 (see Reference 6). While the +9V is available from the Explorer 16 Development Board, the +12V supply must be connected externally to J9 (see Reference 10).

2.2.5 LIN Bus Connectors (J10, J11)

The LIN1 and LIN2 Bus Connectors are 3-pin terminal blocks that connect the ECAN/LIN PICtail Plus Daughter Board to a LIN Bus. LIN1 connector J10 (see Reference 9) connects UART1 module on the dsPIC33F or PIC24 control device to the LIN Bus via the LIN1 Transceiver. LIN2 connector J11 (see Reference 5) connects UART 2 module on the dsPIC33F or PIC24 control device to the LIN Bus via the LIN2 Transceiver.

2.2.6 LIN Transceiver Power Supply Select Jumper (J8)

This 3-pin jumper (see Reference 6) selects the power supply to LIN Transceivers U1 and U2. Either +9V DC is supplied from the Explorer 16 Development Board or +12V DC is supplied from an external power supply connected to J9 (see Reference 10).

2.2.7 External +12V DC LIN Power Supply Terminal Block (J9)

External +12V DC power for the LIN transceivers must be connected to J9 (see Reference 10).

2.2.8 CAN Transceivers (U3, U4)

A pair of MCP2551 CAN Transceivers (see Reference 11 and Reference 14) provide the interface between the ECAN modules on the dsPIC33F or PIC24 control device and the CAN bus. The transceiver converts the signals from the ECAN modules on the dsPIC33F or PIC24 device to a pair of differential CAN Bus signals.

2.2.9 CAN Bus Termination Jumpers (J4, J5)

Termination Jumpers (J4, J5) are provided to terminate the CAN Bus. Jumper J4 (see Reference 12) places a 120 ohm termination resistor across the CAN bus connected to CAN1. Jumper J5 (see Reference 16) places a 120 ohm termination resistor across the CAN bus connected to CAN2.

2.2.10 CAN Bus Connectors (J6, J7)

Nine-pin D-type connectors connect the ECAN/LIN PICtail Plus Daughter Board to a CAN Bus. CAN1 connector J6 (see Reference 13) connects ECAN 1 module on the dsPIC33F or PIC24 control device to the CAN Bus via the CAN Transceiver. CAN2 connector J7 (see Reference 15) connects ECAN 2 module on the dsPIC33F or PIC24 device to the CAN Bus via the CAN Transceiver. The CAN bus provides high and low differential signals.

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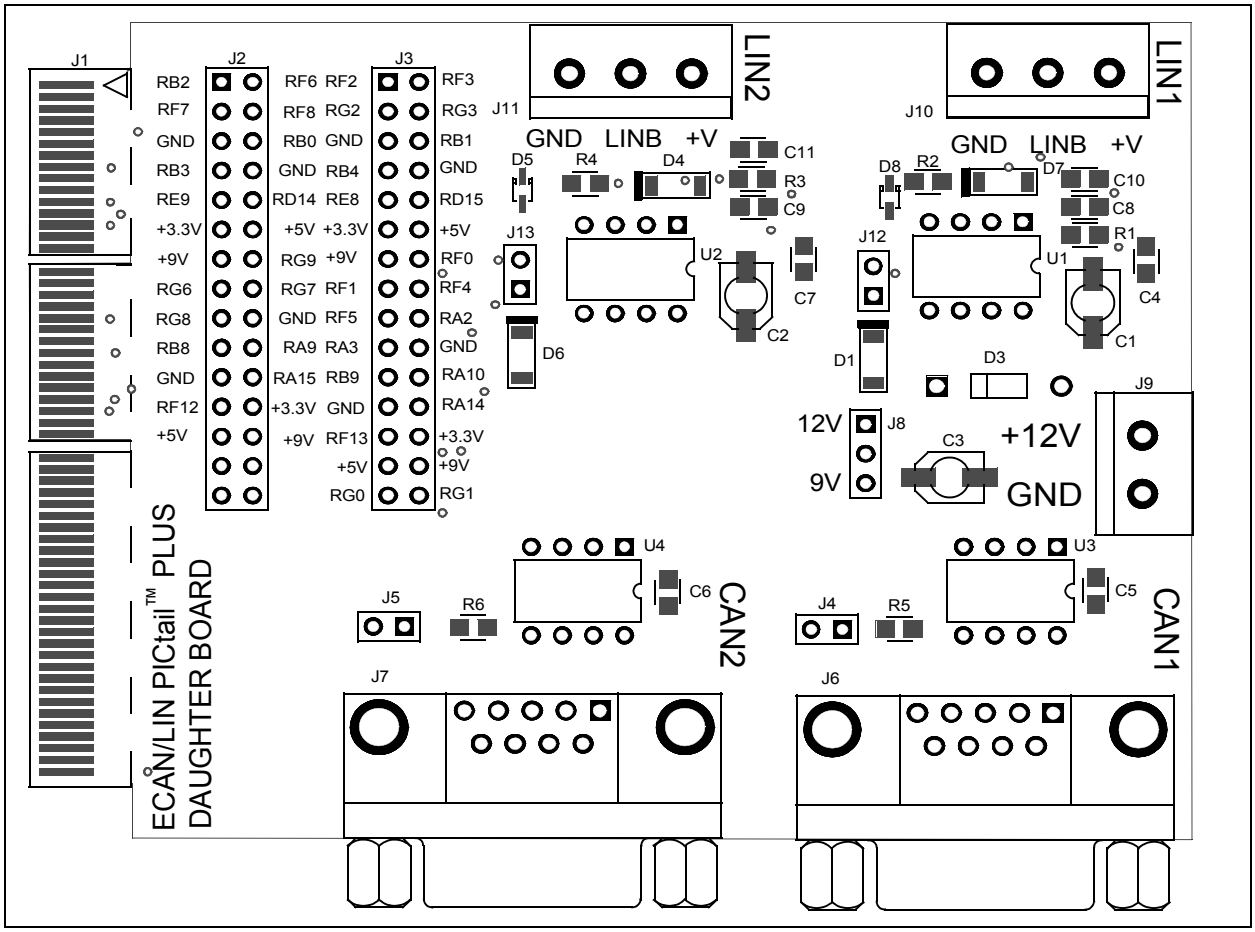
Appendix A. Drawings and Schematics

This appendix provides drawings and schematic diagrams of the ECAN/LIN PICtail Plus Daughter Board.

A.1 ECAN/LIN PICTAIL PLUS DAUGHTER BOARD LAYOUT

Figure A-1 is a drawing of the ECAN/LIN PICtail Plus Daughter Board layout.

FIGURE A-1: ECAN/LIN PICtail™ PLUS DAUGHTER BOARD LAYOUT



A.2 SCHEMATIC DIAGRAMS

The following schematic diagrams are included in this appendix:

- Figure A-2: LIN Circuit Schematics
- Figure A-3: CAN Circuit Schematics
- Figure A-4: Connector and Header Pinout Schematics

FIGURE A-2: LIN CIRCUIT SCHEMATICS

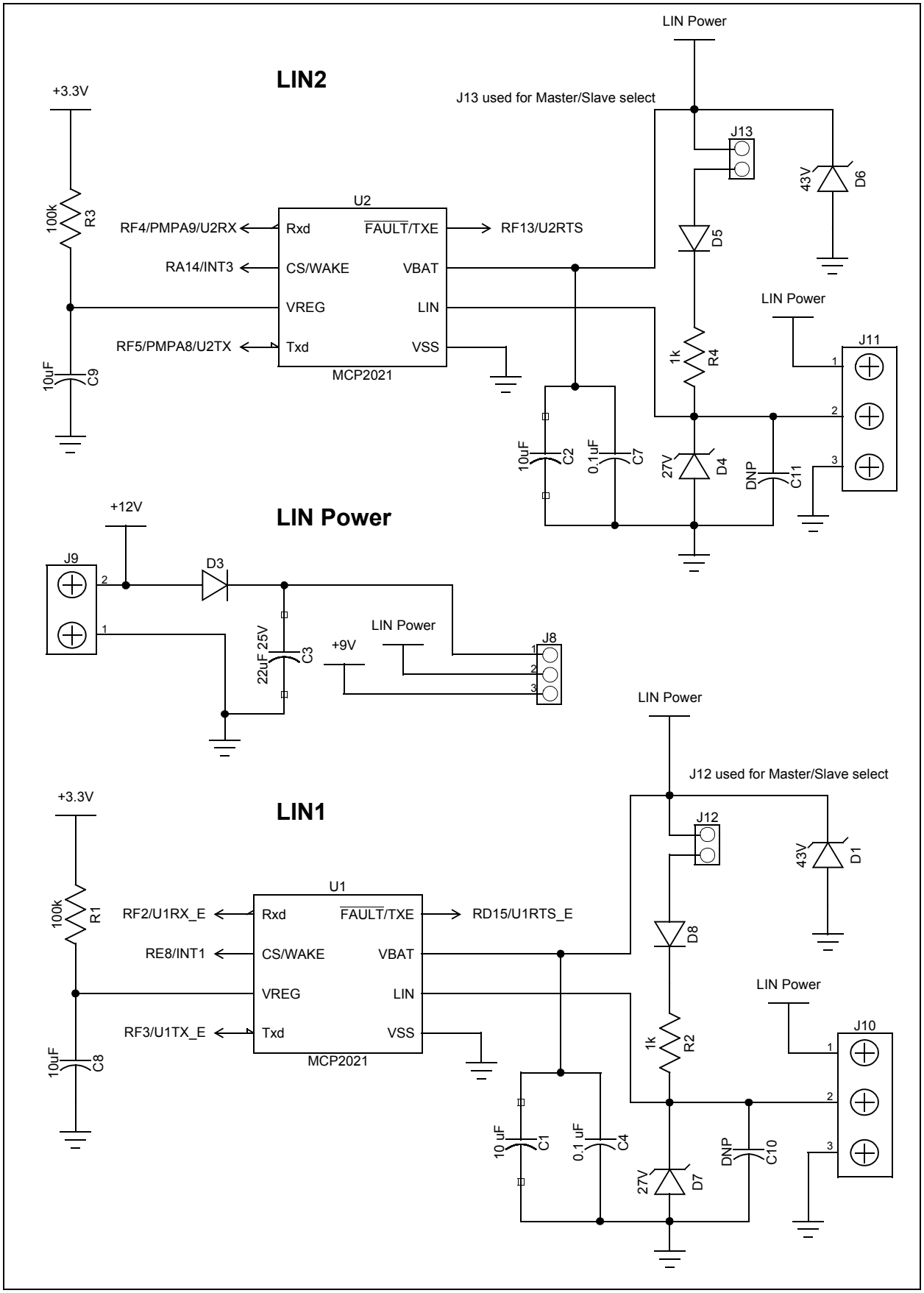


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FIGURE A-3: CAN CIRCUIT SCHEMATICS

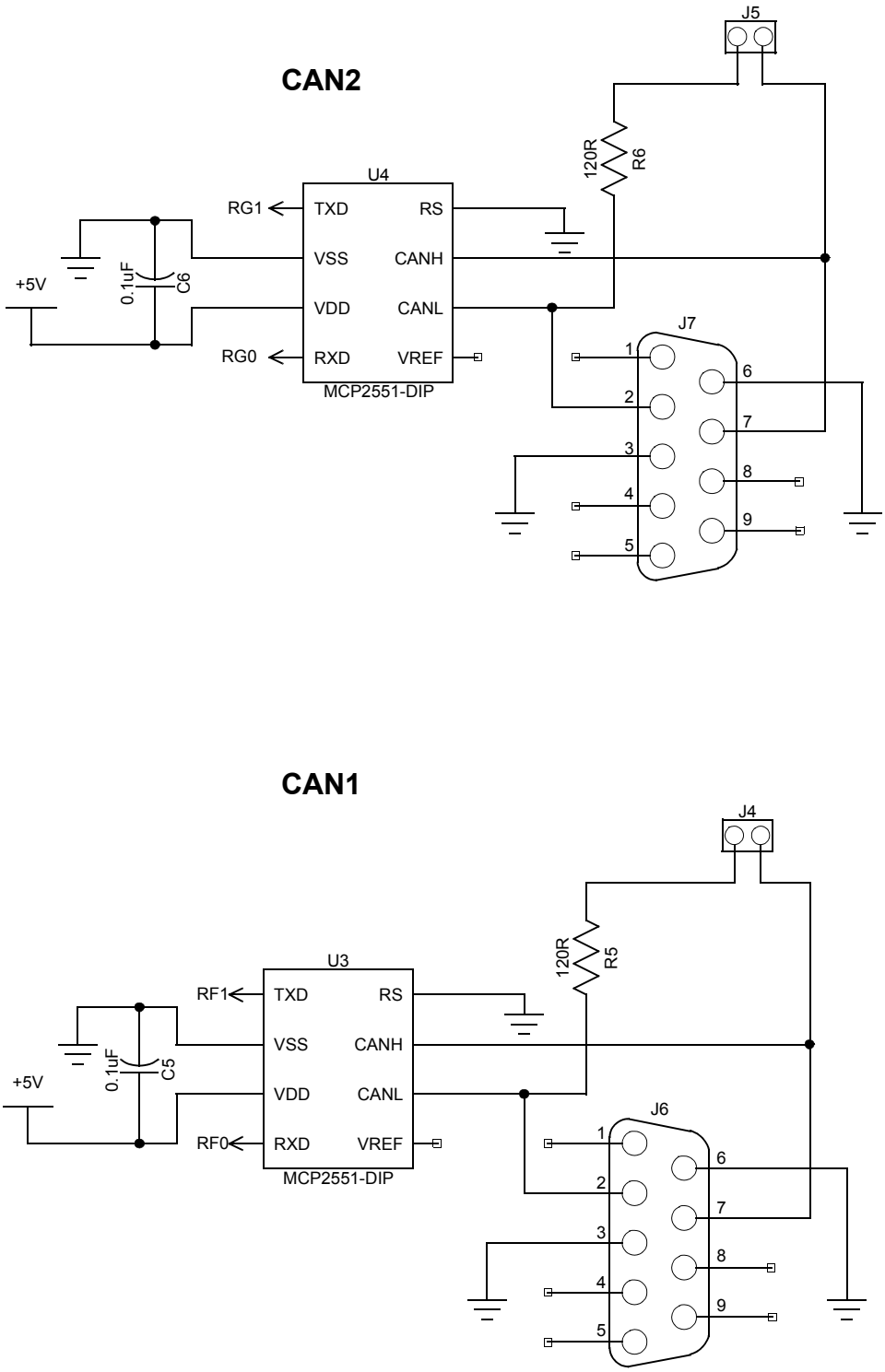
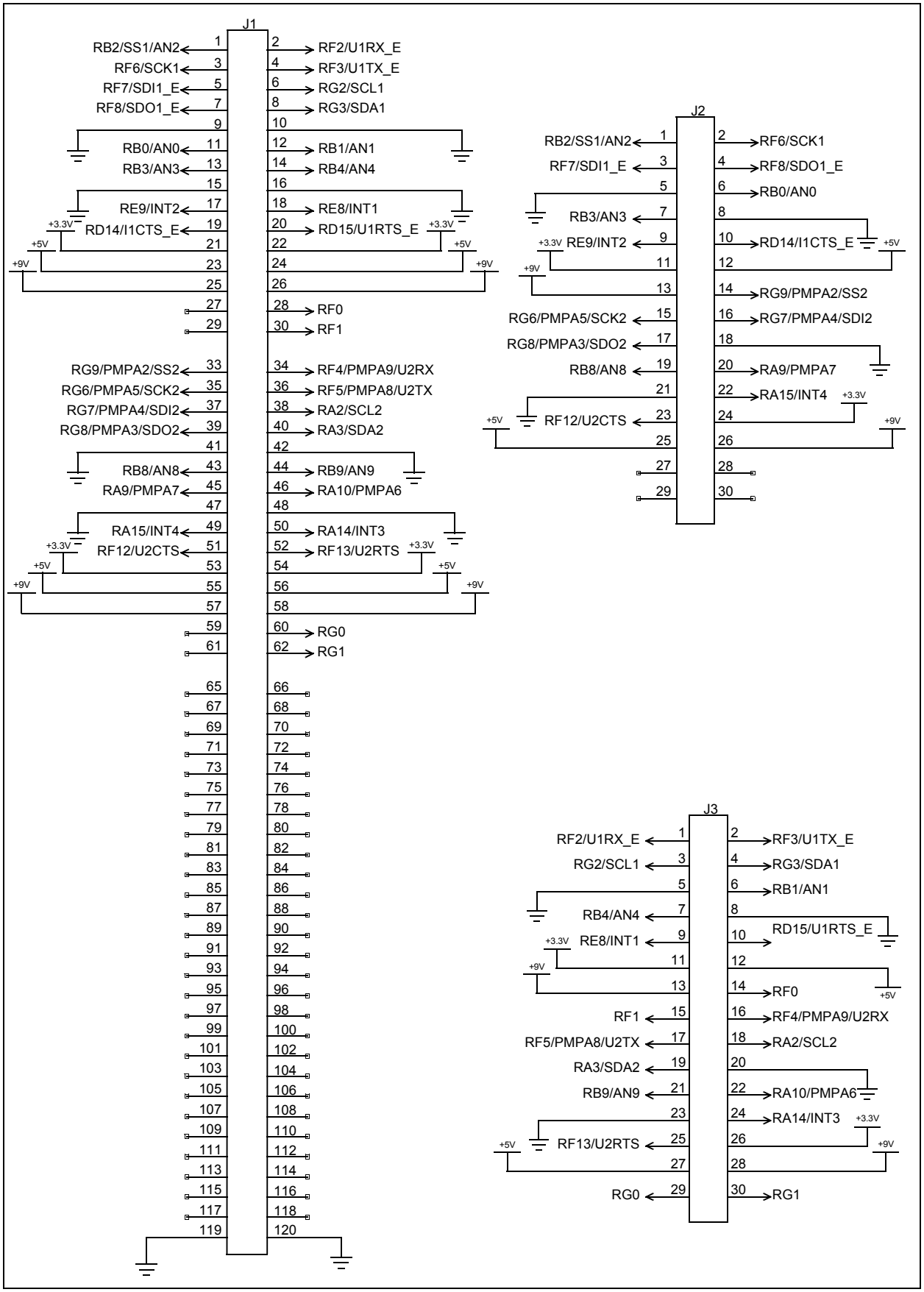


FIGURE A-4: CONNECTOR AND HEADER PINOUT SCHEMATICS



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